FIG. 1

FIG. 2

Applicants: Steven J.E. Wilton et al.   Docket No.: ALT-275   Filed: Herewith
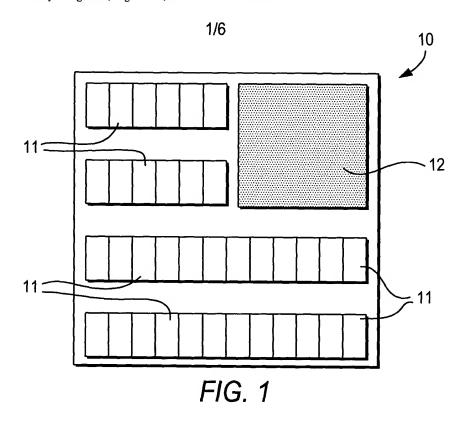Express Mail Label No.: EV132183237US
For: METHOD FOR CONSTRUCTING AN INTEGRATED CIRCUIT DEVICE HAVING FIXED AND PROGRAMMABLE
       LOGIC PORTIONS AND PROGRAMMABLE LOGIC ARCHITECTURE FOR USE THEREWITH
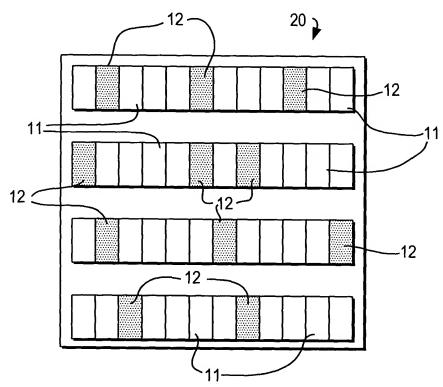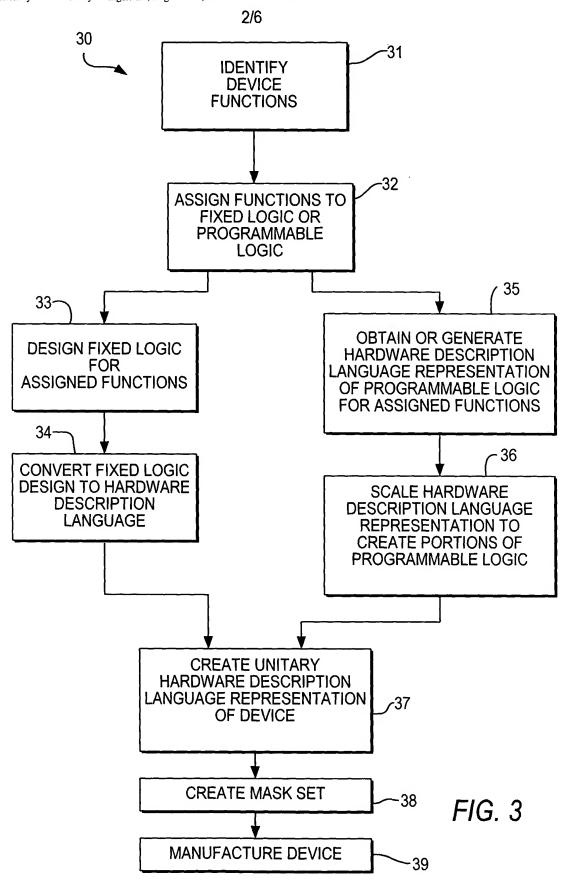Attorney: Jeffrey H. Ingerman, Reg. No. 31,069   Sheet 2 of 6

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IDENTIFY
DEVICE
FUNCTIONS — 31

ASSIGN FUNCTIONS TO
FIXED LOGIC OR
PROGRAMMABLE
LOGIC — 32

33 —
DESIGN FIXED LOGIC
FOR
ASSIGNED FUNCTIONS

35 —
OBTAIN OR GENERATE
HARDWARE DESCRIPTION
LANGUAGE REPRESENTATION
OF PROGRAMMABLE LOGIC
FOR ASSIGNED FUNCTIONS

34 —
CONVERT FIXED LOGIC
DESIGN TO HARDWARE
DESCRIPTION
LANGUAGE

36 —
SCALE HARDWARE
DESCRIPTION LANGUAGE
REPRESENTATION TO
CREATE PORTIONS OF
PROGRAMMABLE LOGIC

CREATE UNITARY
HARDWARE DESCRIPTION
LANGUAGE REPRESENTATION
OF DEVICE — 37

CREATE MASK SET — 38

MANUFACTURE DEVICE — 39

FIG. 3

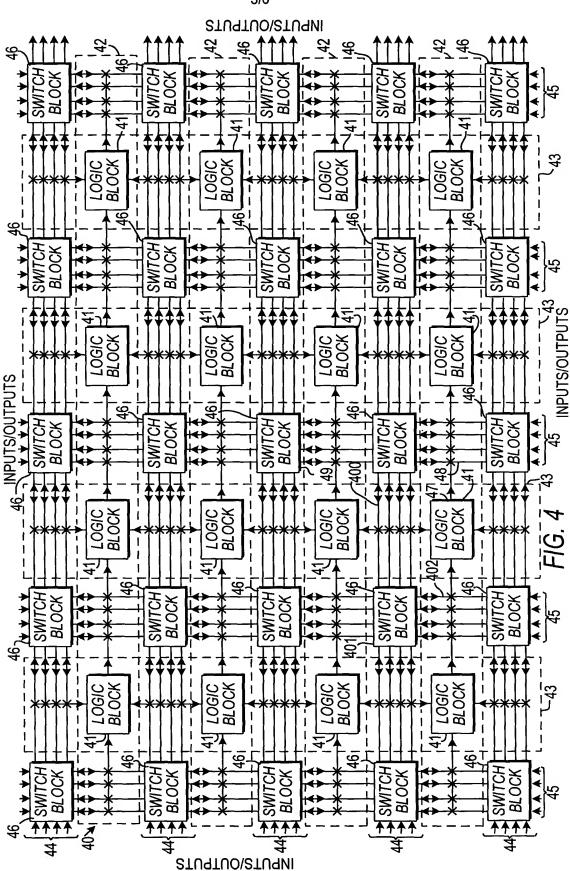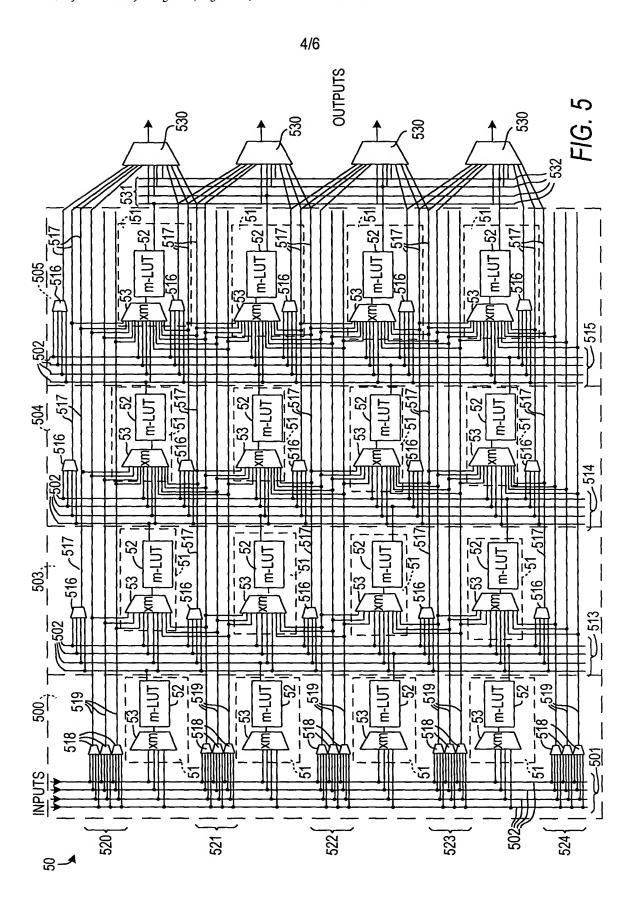Applicants: Steven J.E. Wilton et al.   Docket No.: ALT-275   Filed: Herewith
Express Mail Label No.: EV132183237US
For: METHOD FOR CONSTRUCTING AN INTEGRATED CIRCUIT DEVICE HAVING FIXED AND PROGRAMMABLE
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FIG. 4

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FIG. 5

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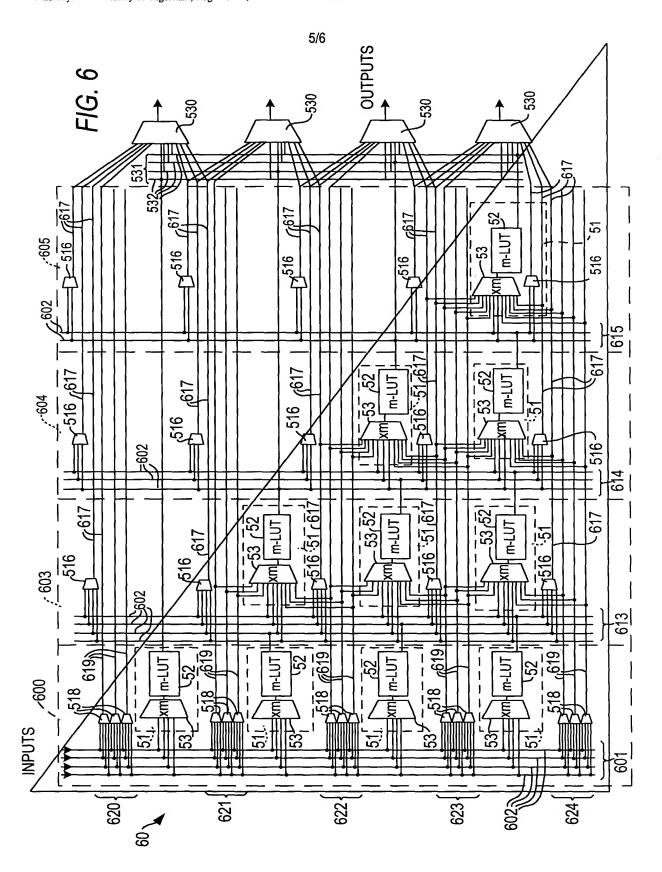Express Mail Label No.: EV132183237US
For: METHOD FOR CONSTRUCTING AN INTEGRATED CIRCUIT DEVICE HAVING FIXED AND PROGRAMMABLE
        LOGIC PORTIONS AND PROGRAMMABLE LOGIC ARCHITECTURE FOR USE THEREWITH
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FIG. 6

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For: METHOD FOR CONSTRUCTING AN INTEGRATED CIRCUIT DEVICE HAVING FIXED AND PROGRAMMABLE
       LOGIC PORTIONS AND PROGRAMMABLE LOGIC ARCHITECTURE FOR USE THEREWITH
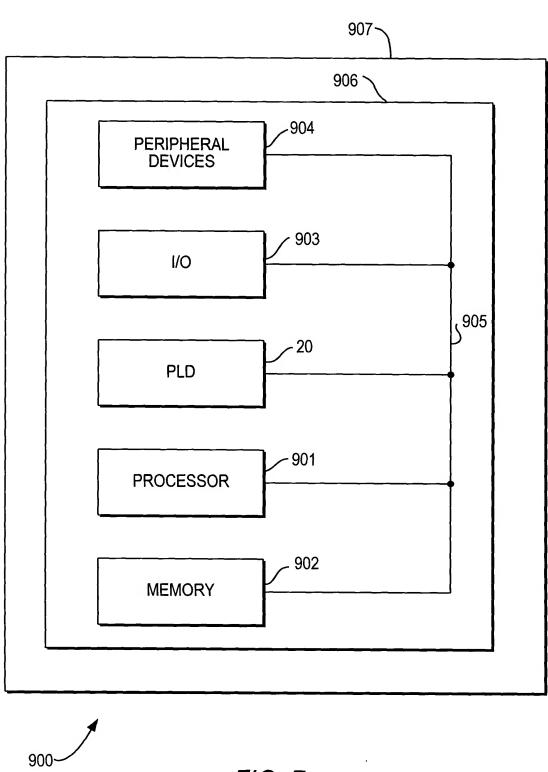Attorney: Jeffrey H. Ingerman, Reg. No. 31,069   Sheet 6 of 6

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FIG. 7